

What is claimed is:

1. A communication device for a target integrated circuit chip having a digital processor, an on-chip emulator for controlling said digital processor and for collecting operation data from said digital processor for communicating to off-chip circuitry, and a target on-chip universal serial bus interface connected to said on-chip emulator, the communication device comprising an Ethernet port, a universal serial bus port and a further integrated circuit chip having on-chip processing circuitry, on-chip memory circuitry, an on-chip Ethernet interface and an on-chip universal serial bus interface, said on-chip Ethernet interface being connected to said Ethernet port, the said interfaces being connected to said processing circuitry for translating between Ethernet protocol data on an Ethernet bus connected to said Ethernet port and universal serial bus data for said target on-chip universal serial bus interface.
2. The device of claim 1 further comprising an on-chip memory interface for connection to memory in said device but external to said chip.
3. The device of claim 1 further comprising modem circuitry for connection of a telephone line to said universal serial bus.
4. The device of claim 3 wherein said modem circuitry comprises a soft modem.
5. The device of claim 3 wherein said modem circuitry comprises a hard modem.
6. The combination of a communication device and a target integrated circuit chip, said target integrated circuit chip having a digital processor, an on-chip emulator for controlling said digital processor and for collecting operation data from said digital processor for communicating to off-chip circuitry, and a target on-chip universal serial bus interface connected to said on-chip emulator, the communication device comprising an Ethernet port for connection to said off-chip circuitry, a universal serial bus port and a further integrated circuit chip having on-chip processing circuitry, on-chip memory

circuitry, an on-chip Ethernet interface and an on-chip universal serial bus interface, said on-chip Ethernet interface being connected to said Ethernet port, the said interfaces being connected to said processing circuitry for translating between Ethernet protocol data on an Ethernet bus connected to said Ethernet port and universal serial bus data for said target on-chip universal serial bus interface.

7. The combination of claim 6 further comprising modem circuitry for connection of a telephone line to said processing circuitry of said communication device.

8. A method of communicating with a target integrated circuit chip having a digital processor, an on-chip emulator for controlling said digital processor and for collecting operation data from said digital processor for communicating to off-chip circuitry, and a target on-chip universal serial bus interface connected to said on-chip emulator, the method comprising:

supplying data from said off-chip circuitry via an Ethernet bus to an Ethernet port of a communication device comprising a further integrated circuit chip having on-chip Ethernet interface circuitry and on-chip processing circuitry;

passing said data as an input said Ethernet interface circuitry; in said Ethernet interface circuitry, translating said data into a form suitable for said on-chip processing circuitry;

supplying said translated data to said on-chip processing circuitry;

processing said translated data to provide output data;

applying said output data to an on-chip universal serial bus interface, for transfer via a universal serial bus to said on-chip emulator of said target integrated circuit chip.

9. A method of debugging a target integrated circuit chip using a host computer device, said target integrated circuit having a digital processor and an on-chip emulator wherein said on-chip emulator is operable to control said digital processor according to a host program and to collect operation data from said digital processor for communicating to said host, said chip comprising a target on-chip universal serial bus interface connected to said on-chip emulator, the method comprising

providing a communication device comprising an Ethernet port, a universal serial bus port and a further integrated circuit chip having on-chip processing circuitry, on-chip memory circuitry, an on-chip Ethernet interface and an on-chip universal serial bus interface,

connecting said Ethernet port to said host via an Ethernet link;

connecting said communication device to said target on-chip universal serial bus interface via a universal serial bus;

communicating data between said on-chip emulator and said on-chip processing circuitry;

processing data in said on-chip processing circuitry to provide output data; and

supplying said output data to said host via said Ethernet port.

10. The method of claim 9 and further comprising loading a program from said host to said on-chip processing circuitry over said Ethernet link.

11. A method of debugging a target integrated circuit chip having a digital processor and an on-chip emulator wherein said on-chip emulator is operable to control said digital processor and to collect operation data from said digital processor for communicating to a host, said chip comprising a target on-chip universal serial bus interface connected to said on-chip emulator, the method comprising

providing a communication device comprising an Ethernet port, a universal serial bus port and a further integrated circuit chip having on-chip processing circuitry, on-chip memory circuitry, an on-chip Ethernet interface and an on-chip universal serial bus interface,

connecting said Ethernet port to said host;

connecting said communication device to said target on-chip universal serial bus interface via a universal serial bus;

communicating data between said on-chip emulator and said on-chip processing circuitry;

processing said data in said on-chip processing circuitry to provide output data;

supplying said output data to said on-chip emulator circuitry.

12. The method of claim 9 comprising running an embedded web-server process on said on-chip processing circuitry.

13. The method of claim 12 comprising running the embedded web-server process on the on-chip processing circuitry in response to a request from a client device.